

Asst. Prof. Sadiye Nergis TURAL POLAT

Personal Information

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Education Information

Doctorate, Yıldız Teknik Üniversitesi, Faculty of Electrical & Electronics, Elektronik Ve Haberleşme Mühendisliği, Turkey
2002 - 2009

Post Graduate, Yıldız Teknik Üniversitesi, Faculty of Electrical & Electronics, Elektronik Ve Haberleşme Mühendisliği,
Turkey 1999 - 2002

Under Graduate, Yıldız Teknik Üniversitesi, Faculty of Electrical & Electronics, Elektronik Ve Haberleşme Mühendisliği,
Turkey 1994 - 1998

Foreign Languages

English, C1 Advanced

French, B1 Intermediate

Dissertations

Doctorate, UZAYSAL VE UZAY-ZAMANSAL HÜCRESEL SINIR AĞI FİLTRELERİ, Yıldız Teknik Üniversitesi, Graduate
School of Natural and Applied Sciences, Elektronik Ve Haberleşme Mühendisliği/Elektronik, 2009

Post Graduate, TÜBİTAK YİTAL 1.5um TEKNOLOJİSİ İLE 6 BİT FLASH ADC TASARIMI, Yıldız Teknik Üniversitesi,
Graduate School of Natural and Applied Sciences, Elektronik Ve Haberleşme Mühendisliği/Elektronik, 2002

Research Areas

Electrical and Electronics Engineering, Electronic, Printed Circuits, Thin Film, Thick Film and Hybrid ICs, Electronic
Circuits, Nanotechnology, Engineering and Technology

Academic Titles / Tasks

Assistant Professor, Yıldız Teknik Üniversitesi, Elektrik/ Elektronik, Elektronik Ve Haberleşme Müh., 2018 - Continues

Professional Experience

Head of ADEK Commission, Yıldız Teknik Üniversitesi, 2017 - Continues

Member of ADEK Commission, Yıldız Teknik Üniversitesi, 2014 - 2017

Courses

Temel Elektronik Devreleri, Under Graduate, 2017 - 2018

SEMICONDUCTOR PHYSICS, Under Graduate, 2016 - 2017, 2017 - 2018

ELECTRONIC CIRCUITS 1, Under Graduate, 2017 - 2018

Mesleki İngilizce 1, Under Graduate, 2016 - 2017

Algoritmalar ve Programlama (2 Grup), Under Graduate, 2016 - 2017

Articles Published in Journals That Entered SCI, SSCI and AHCI Indexes

- **Third-Kind Chebyshev Wavelet Method for the Solution of Fractional Order Riccati Differential Equations**
Tural-Polat S. N.
Journal of Circuits, Systems and Computers, 2019 (Journal Indexed in SCI)
- **The vector-matrix form numerical simulations for time-derivative cellular neural networks**
Polat S. N.
INTERNATIONAL JOURNAL OF NUMERICAL MODELLING-ELECTRONIC NETWORKS DEVICES AND FIELDS, vol.31, 2018 (Journal Indexed in SCI)
- **A Complete Embedded System Design of Breakout Game**
Tural-Polat S. N.
JOURNAL OF CIRCUITS SYSTEMS AND COMPUTERS, vol.26, 2017 (Journal Indexed in SCI)
- **Efficient Simulation of Time-Derivative Cellular Neural Networks**
Polat S. N. T. , Yavuz O., Tavsanoğlu V.
IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I-REGULAR PAPERS, vol.59, pp.2638-2645, 2012 (Journal Indexed in SCI)

Articles Published in Other Journals

- **VHDL İLE TAM GÖMÜLÜ BİR TETRİS OYUNU GERÇEKLEŞTİRMESİ**
Tural Polat S. N.
Ömer Halisdemir Üniversitesi Mühendislik Bilimleri Dergisi, vol.9, pp.128-136, 2020 (Other Refereed National Journals)
- **Pong Game as an Embedded System**
TURAL POLAT S. N.
International Journal of Advancements in Electronics and Electrical Engineering, vol.4, pp.38-42, 2015 (Refereed Journals of Other Institutions)
- **Hareket Algılayıcı Filtreler**
TURAL POLAT S. N. , TAVŞANOĞLU A. V.
Sigma Journal of Engineering and Natural Sciences, 2009 (Other Refereed National Journals)

Refereed Congress / Symposium Publications in Proceedings

- **Numerical Solutions of Fractional Differential Equations Using Shifted Chebyshev Polynomials of the Fourth Kind**
TURAL POLAT S. N.
IRES 836th International Conference on Innovative Engineering Technologies (ICIET 2020), ST PETERSBURG, Russia, 9 - 10 May 2020, pp.1-4
- **Bernoulli Polynomial Method for the Solution of Linear Fractional Differential Equations**
TURAL POLAT S. N.
IRES 830th International Conference on Engineering and Natural Science (ICENS) 2020, Paris, France, 25 - 26 April 2020, pp.51-53
- **Pong Game as an Embedded System**
TURAL POLAT S. N.
2nd International Conference on Advances in Computing Electronics and Communication, Zürich, Switzerland, 25 - 26 October 2014
- **3-D spatio-temporal Gabor-type filter implementations with Time-Derivative Cellular Neural Networks**
Yavuz O., TURAL POLAT S. N. , Cesur E., TAVŞANOĞLU A. V.

European Conference on Circuit Theory and Design (ECCTD), Dresden, Germany, 8 - 12 September 2013

- **2nd order 2-D spatial filters and Cellular Neural Network implementations**

Tavsanoglu V., TURAL POLAT S. N.

2012 13th International Workshop on Cellular Nanoscale Networks and their Applications, CNNA 2012, Turin, Italy, 29 August 2012

- **ON THE SIMULATION OF TIME DERIVATIVE CELLULAR NEURAL NETWORKS**

Yavuz O., TURAL POLAT S. N. , TAVŞANOĞLU A. V.

18TH EUROPEAN SIGNAL PROCESSING CONFERENCE (EUSIPCO-2010), Aalborg, Denmark, 23 - 27 August 2010

- **A new simulation method for Time-Derivative Cellular Neural Networks**

TURAL POLAT S. N. , TAVŞANOĞLU A. V.

17th European Signal Processing Conference, Glasgow, United Kingdom, 24 - 28 August 2009

- **A Master-Client Type Mapping Solution for Autonom Explorer Robots, Based on Simple Sensing Technologies**

TURAL POLAT S. N. , Özyılmaz L.

2nd International Conference "From Scientific Computing to Computational Engineering

- **A Computer Controlled Mobile Robot Design**

TURAL POLAT S. N. , Polat E., Özyılmaz L.

2nd International Conference "From Scientific Computing to Computational Engineering

Citations

Total Citations (WOS):4

h-index (WOS):1

Scholarships

Başarılı Lisans Son Sınıf Öğrencilerini Destekleme Bursu, Special Institutions and Organizations, 1997 - 1998

Awards

TURAL POLAT S. N. , Lisans Mezuniyeti İkincilik Derecesi, Yıldız Teknik Üniversitesi, June 1998