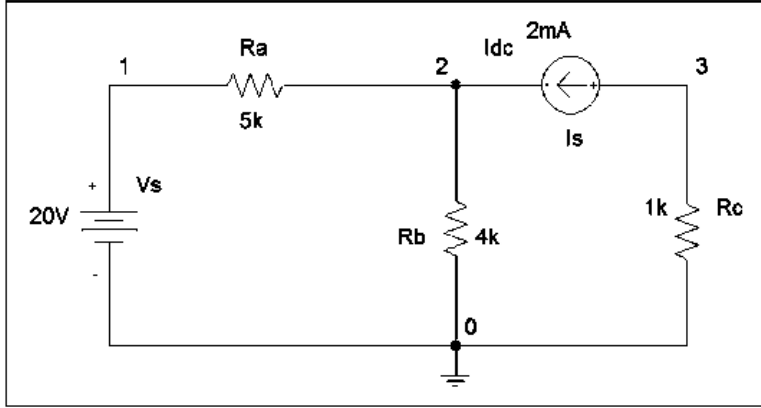


PSPICE NETLIST ÖRNEK UYGULAMALAR

Başlık satırının başına * koymayı unutmayınız.

Örnek 1



Ornek_1 ornek.CIR

Vs 1 0 DC 20.0V

Ra 1 2 5.0k

Rb 2 0 4.0k

Rc 3 0 1.0k

Is 3 2 DC 2.0mA

.TRAN 0.1ms 10ms

.PROBE

.END

Örnek 2

ac circuit

V1 1 0 AC 5v

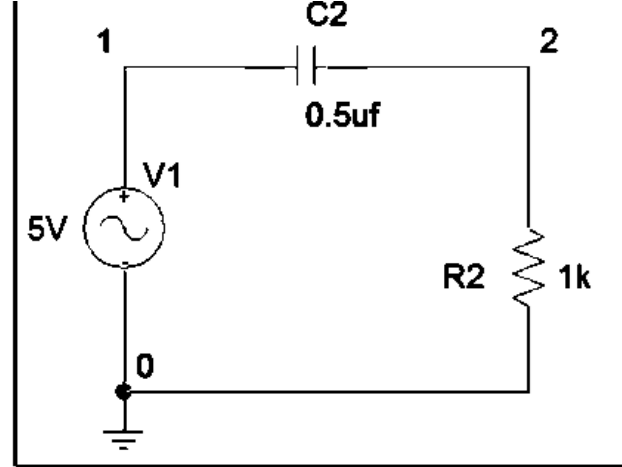
C1 1 2 0.5uF

R1 2 0 1k

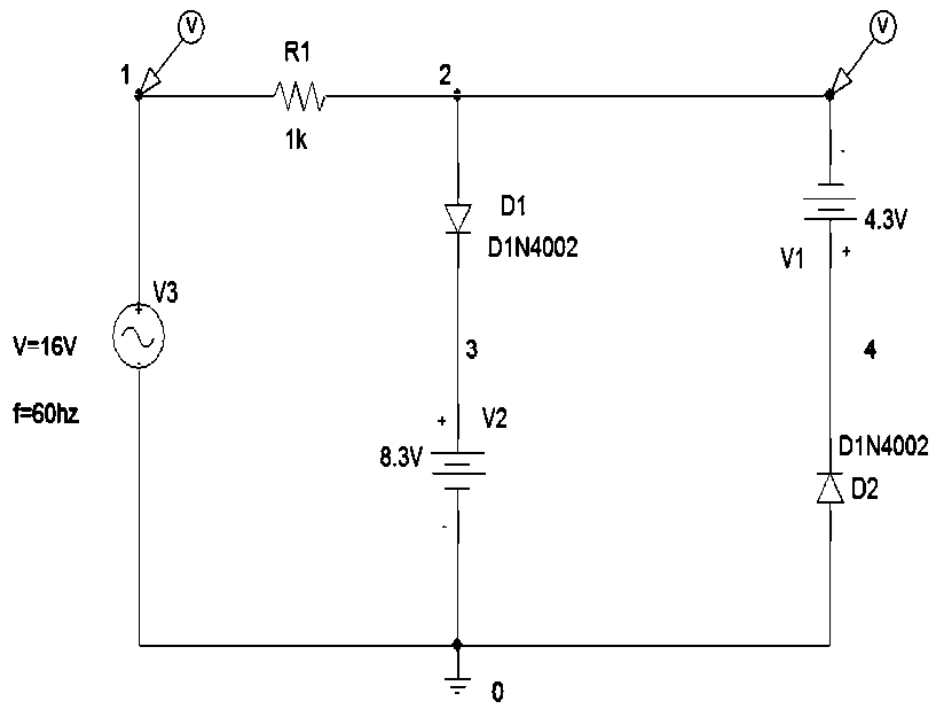
.AC OCT 50 10hz 10meghz

.PROBE

.END

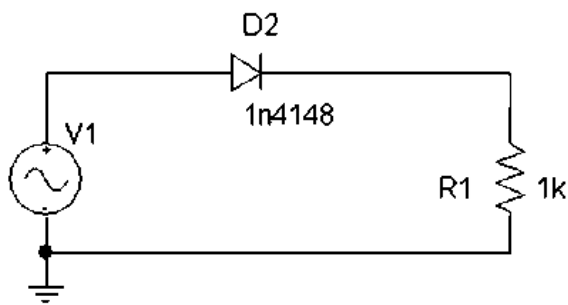


Örnek 3



```
v1 1 0 sin(0 16 60hz)
v2 3 0 dc 8.3v
v3 4 2 dc 4.3v
r1 1 2 1k
d1 2 3 ddiyot
d2 0 4 ddiyot
.model ddiyot D (IS=14.11E-9 RS=33.89E-3
BV=100V IBV=10)
.probe
*tran tadim tson tbaslangic tstep ceiling
.tran 1us 100ms 0ms 0.01ms
.end
```

Örnek 4



```
v1 1 0 sin(0 5 1khz)
r1 2 0 10k
d1 1 2 dname
.model dname D (IS=3.93E-9
RS=1 BV=100V IBV=5E-6)
.probe
*tran tadim tson tbaslangic
tstep ceiling
.tran 1us 5ms 0ms 0.01ms
.end
```