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### **Published journal articles indexed by SCI, SSCI, and AHCI**

**I. Efficient Simulation of Time-Derivative Cellular Neural Networks**

Polat S. N. T., Yavuz O., Tavsanoğlu V.

IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I-REGULAR PAPERS, vol.59, no.11, pp.2638-2645, 2012 (SCI-Expanded)

### **Refereed Congress / Symposium Publications in Proceedings**

**I. 2nd order 2-D spatial filters and Cellular Neural Network implementations**

Tavsanoğlu V., TURAL POLAT S. N.

2012 13th International Workshop on Cellular Nanoscale Networks and their Applications, CNNA 2012, Turin, Italy, 29 August 2012

**II. Demonstration of the Second Generation Real-Time Cellular Neural Network Processor: RTCNNP-v2**  
TAVŞANOĞLU A. V.

Cellular Nanoscale Networks and Their Applications (CNNA), 2012 13th International Workshop on, 01 August 2012

**III. Demo: An improved FPGA implementation of CNN Gabor-type Filters**

TAVŞANOĞLU A. V.

Cellular Nanoscale Networks and Their Applications (CNNA), 2012 13th International Workshop on, 01 August 2012

**IV. On the teaching of the axiomatics of physical systems**

TAVŞANOĞLU A. V.

Circuits and Systems (ISCAS), 2012 IEEE International Symposium on, 01 May 2012

**V. A new control structure for the pipelined CNN processor arrays**

TAVŞANOĞLU A. V.

Cellular Nanoscale Networks and Their Applications (CNNA), 2010 12th International Workshop on, 01 February 2010

### **Metrics**

Publication: 6

Citation (WoS): 3

Citation (Scopus): 6

H-Index (WoS): 1

H-Index (Scopus): 1